

S/N: 10/776,101
Docket No.: UMI-360
Title: HIGH SPEED POWER-GATING
TECHNIQUE FOR INTEGRATED CIRCUIT
DEVICES INCORPORATING A SLEEP MODE OF
OPERATION
Inv: Kim C. Hardee

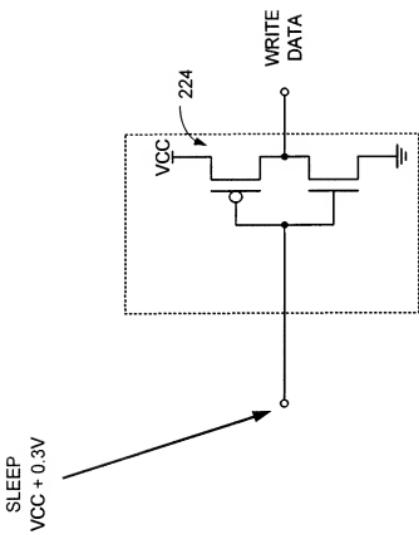


Fig. 3